

**SWITCHED CAPACITOR CIRCUIT CAPABLE OF MINIMIZING CLOCK
FEEDTHROUGH EFFECT AND HAVING LOW PHASE NOISE AND METHOD
THEREOF**

Appl. No. : 10/710,472 Confirmation No. 4471
Applicant : En-Hsiang Yeh
Filed : July 14, 2004
TC/A.U. : 2826
Examiner : Erdem, Fazli
Docket No. : MTKP0157USA4
Customer No. : 27765

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

5

INTRODUCTORY COMMENTS

In response to the Office action of 11/17/2006, please amend the above-identified application as follows:

Remarks begin on page 2 of this paper.